

Reliability of eWLB (embedded wafer level BGA) for Automotive Radar Applications

by

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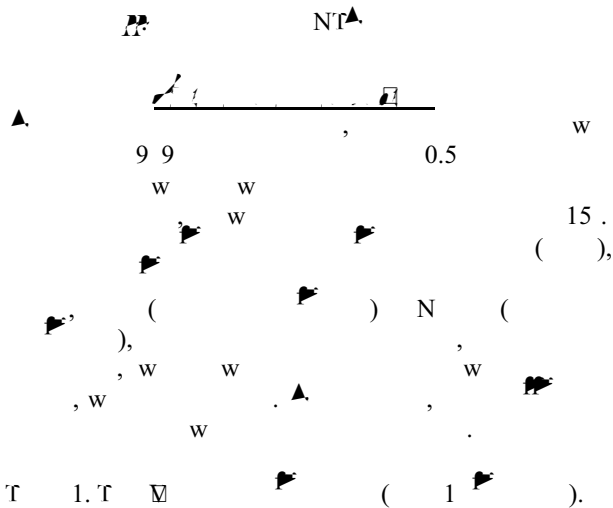
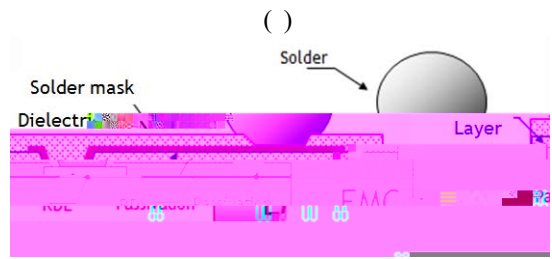
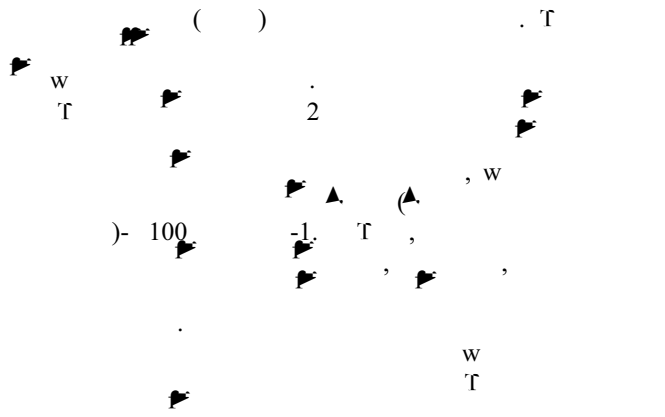
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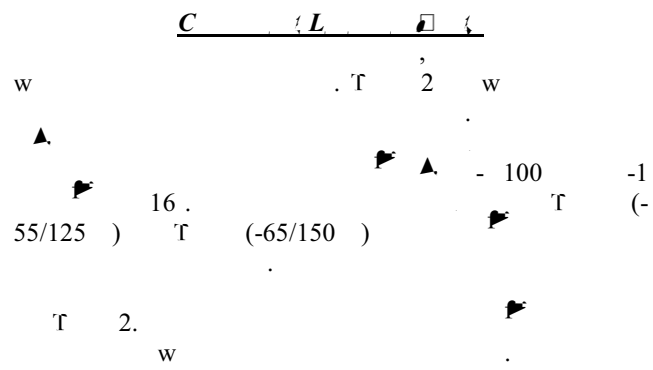
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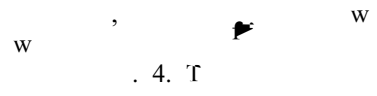
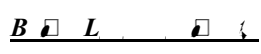
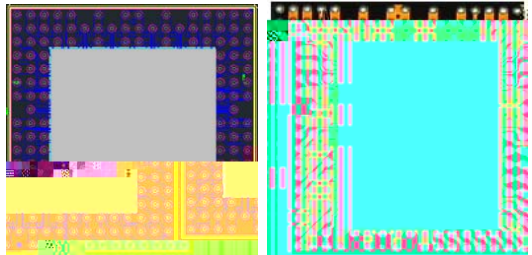
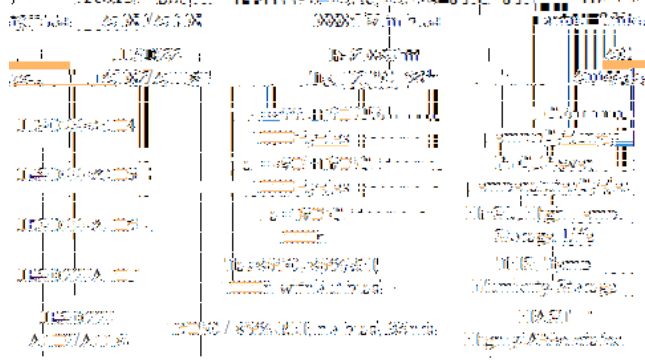


Micrograph of a die showing a grid of pads.

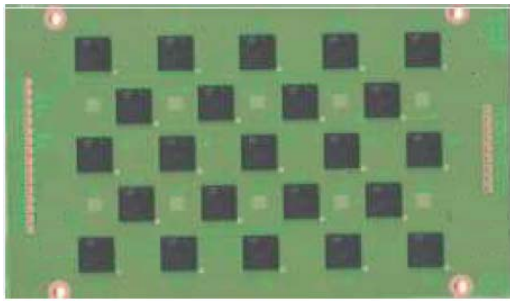
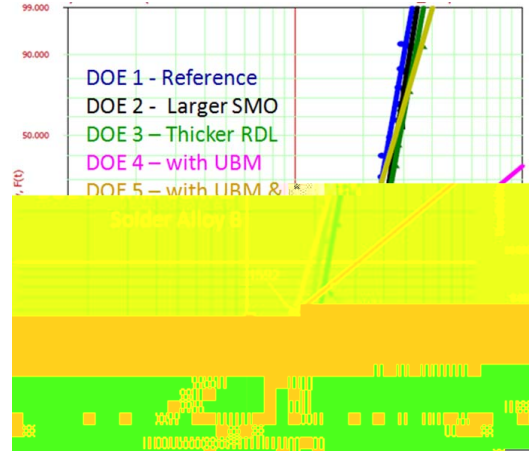
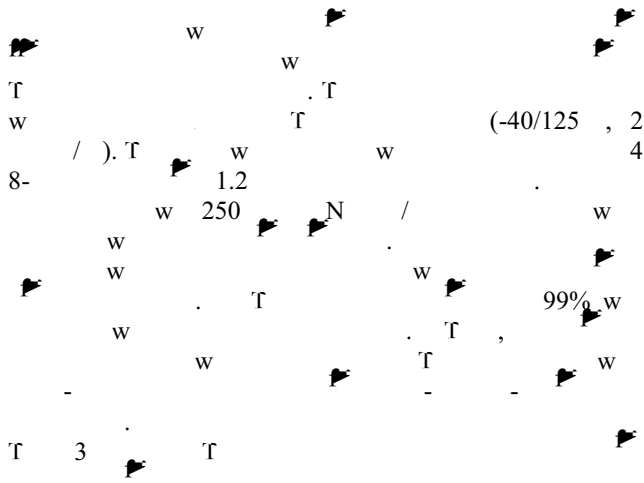


Items	Specification
PKG size	9x9mm
Die Size	6x6mm
Ball Pitch	0.5mm
Ball Dia	0.3mm
IO No.	230
UBM	No
Cu RDL No.	1 layer
Die Thickness	440μm
UBM Thickness	700μm

Test	Q100 Test Condition	Test Conditions
PC Pre-Cond	JEDEC J-STD-020	MSL1 24h bake @ 125°C 168h @ 85C/85%RH Reflow simulation (3times) with Lead free profile Tmax=260°C



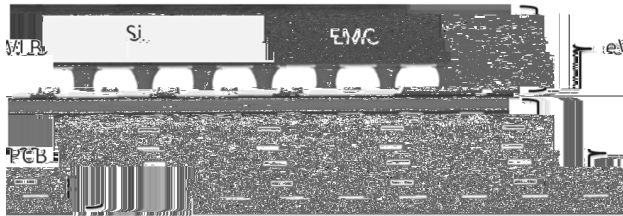
Micrograph of a die showing a grid of pads.



Micrograph showing a grid of square pads on a PCB. The pads are arranged in a regular pattern, and the background is a dark color.

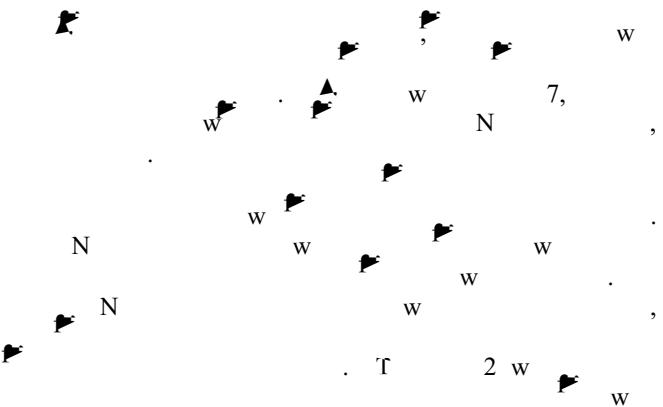
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		First Failure Cycles	
		SMD	NSMD
DOE1	Reference	1452	1847
DOE2	Larger solder mask opening	1388	2245
DOE3	Thicker RDL	1763	2216
DOE4	With UBM+RDL	964	1005
	With UBM and Solder		



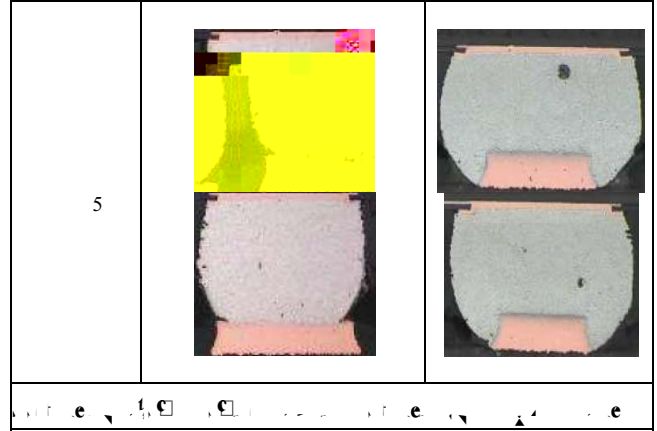
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Micrograph showing a grid of square pads on a PCB. The pads are arranged in a regular pattern, and the background is a dark color.



- w:
1. NSMD board pads showed improved TCoB performance versus SMD ones in all DOE legs.
 2. Larger solder mask opening was effective to improve results for the NSMD pad.
 3. Thicker Cu RDL showed 15~20% improvement in TCoB performance.

	Image 1	Image 2
1		
2		
3		
4		



17,18,19 .

the reliability of WLCSP is dependant of a range of various design factors, for example:

- Dielectric materials property
- Dielectrics thickness



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